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FIG. 1

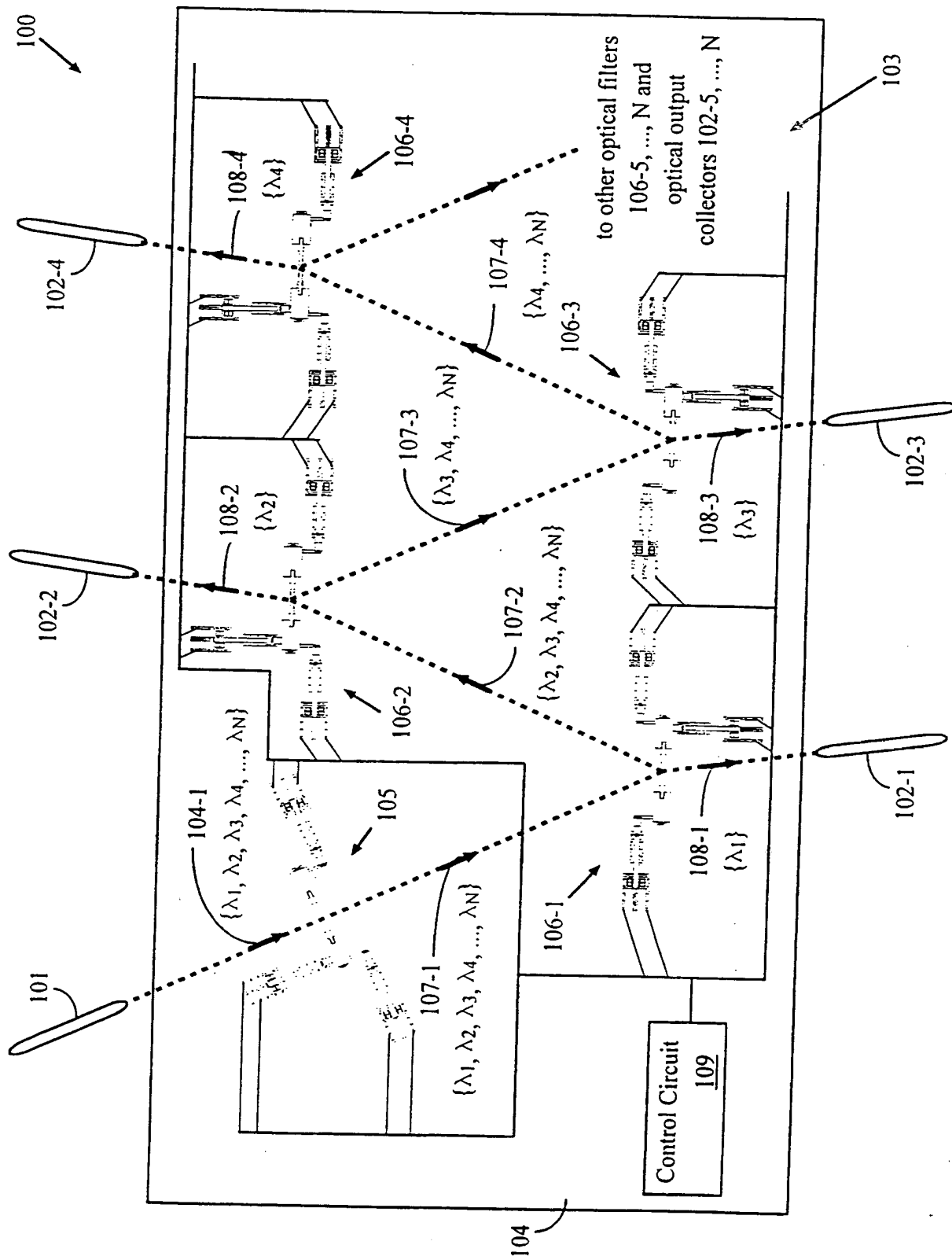


Fig. 1

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FIG. 2 - 53660001

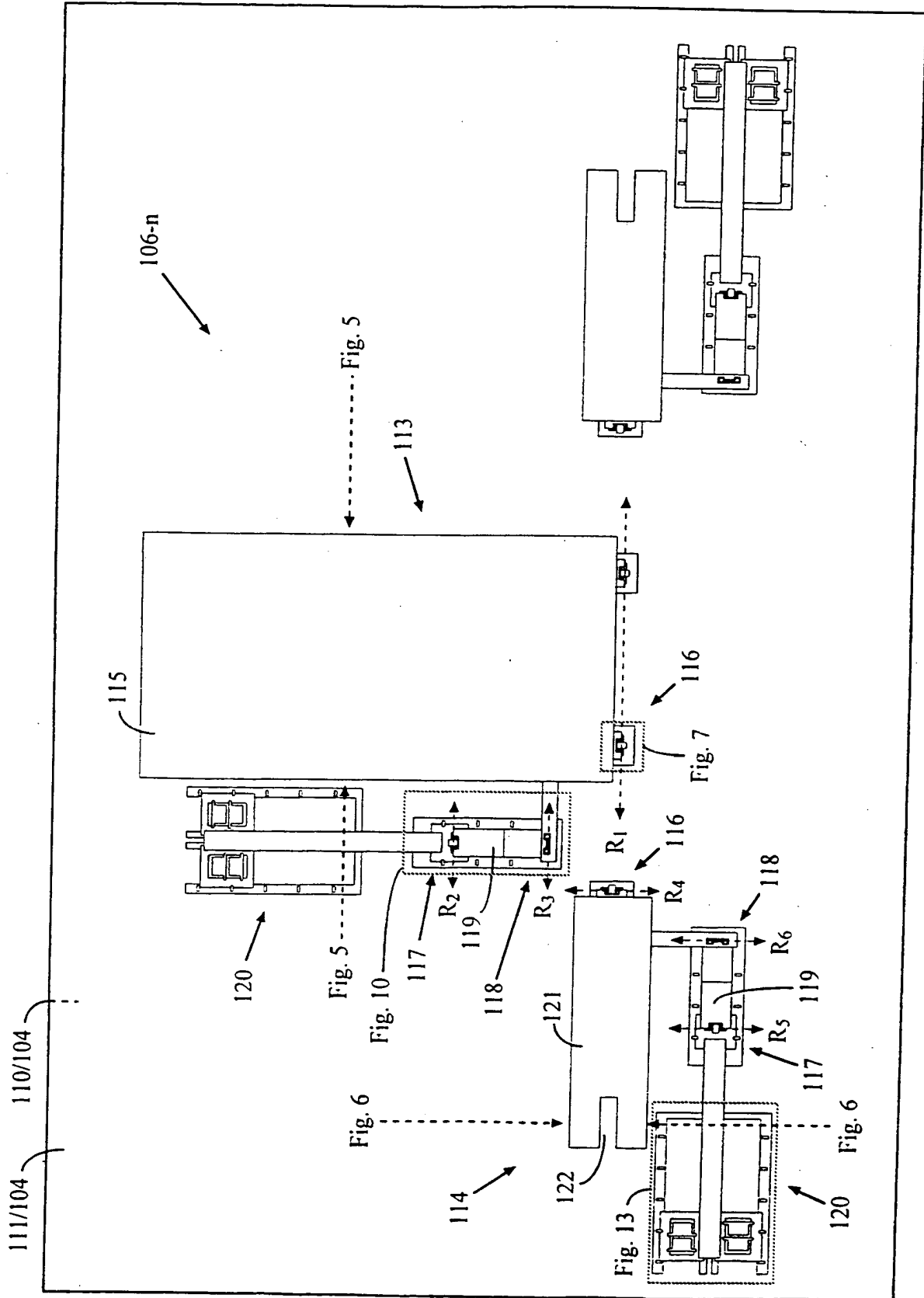


Fig. 2

101121 5366001

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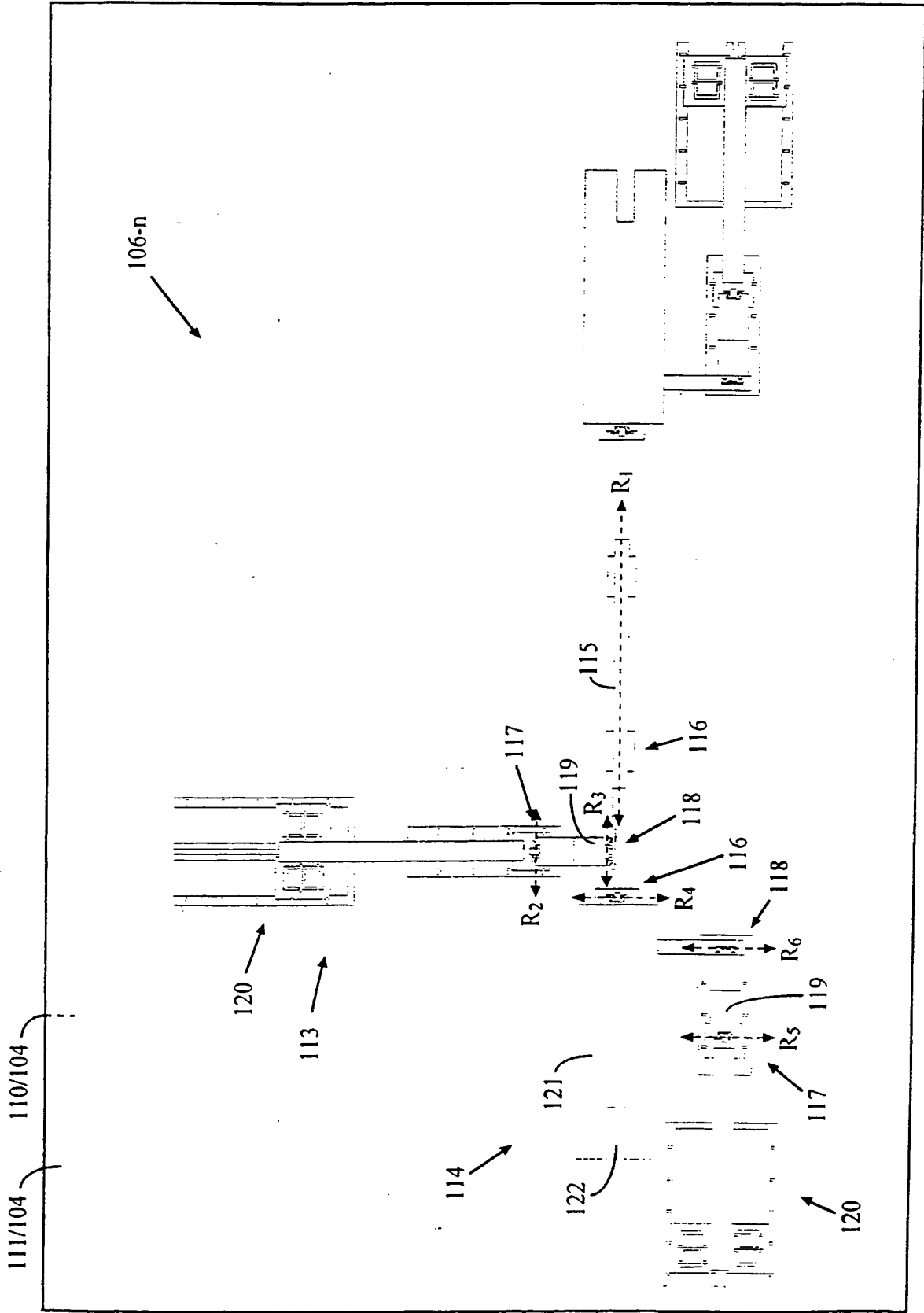


Fig. 3

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FIG. 4

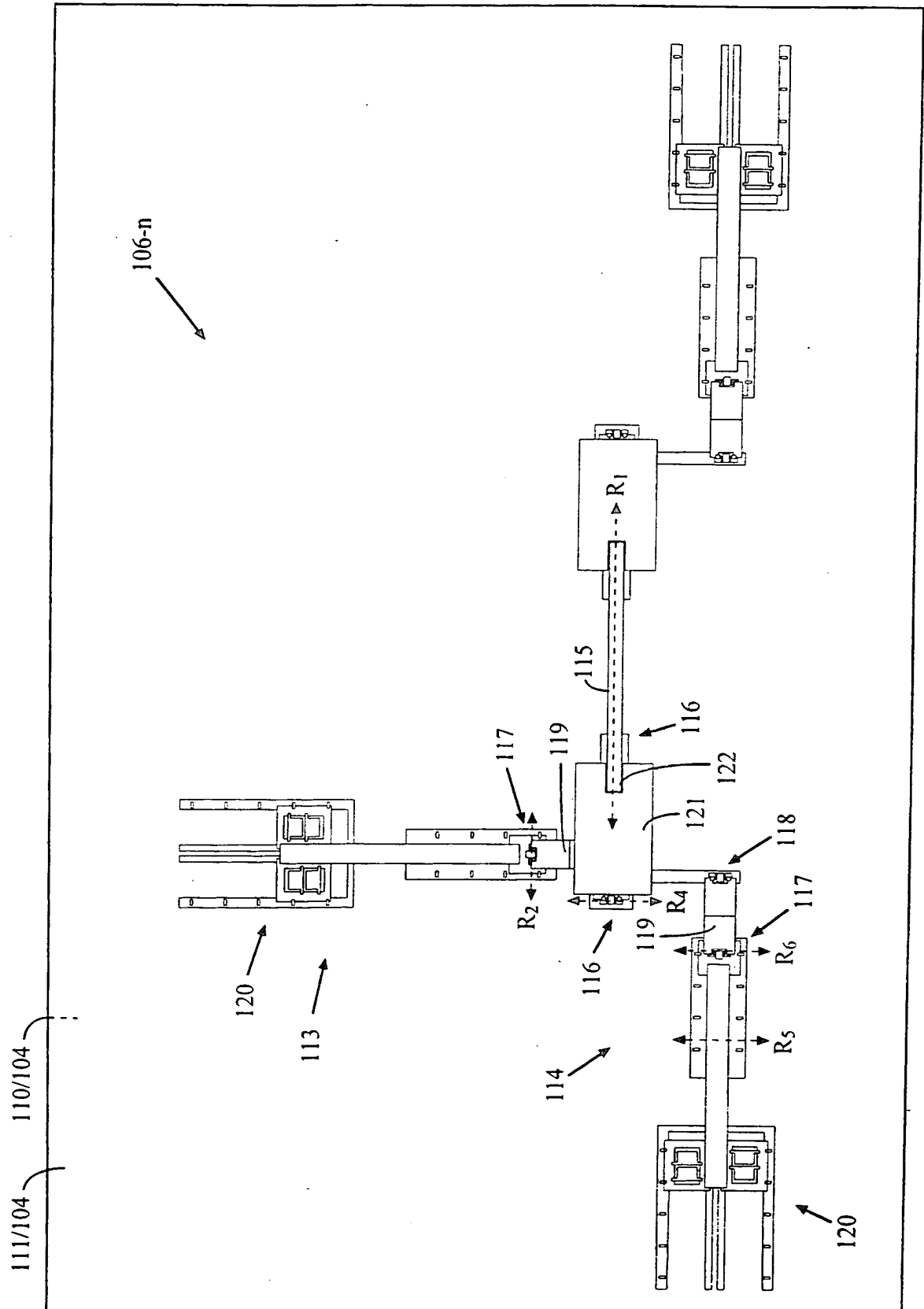
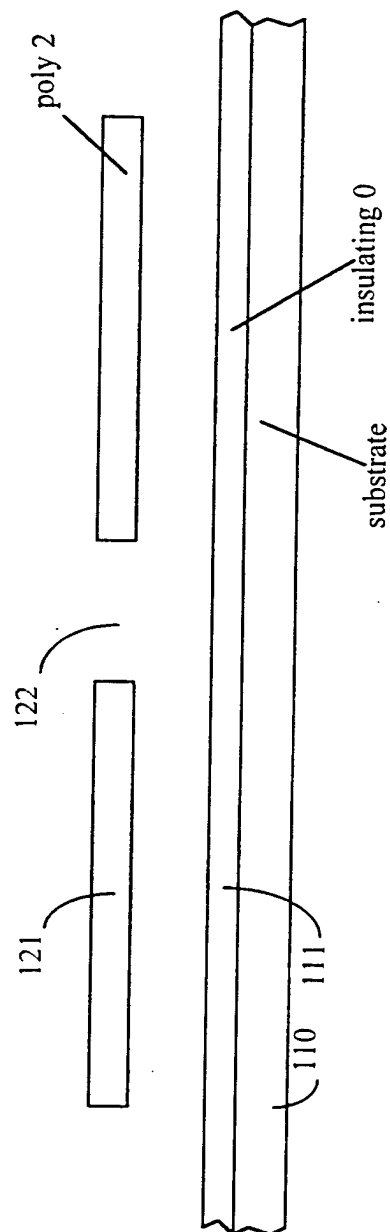
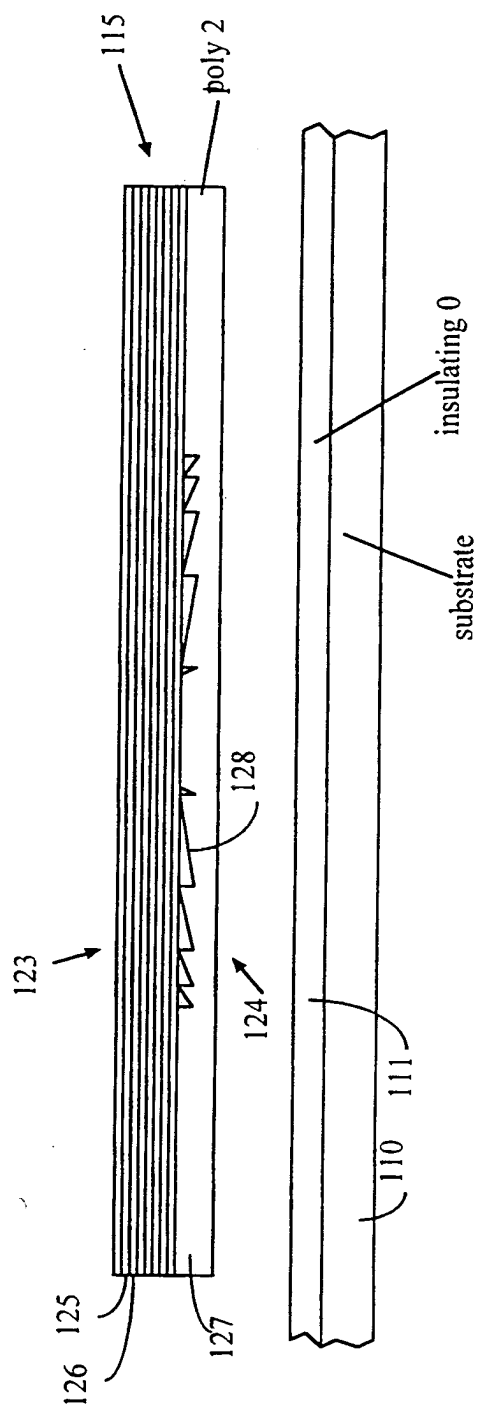


Fig. 4

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Fig. 5

Fig. 6





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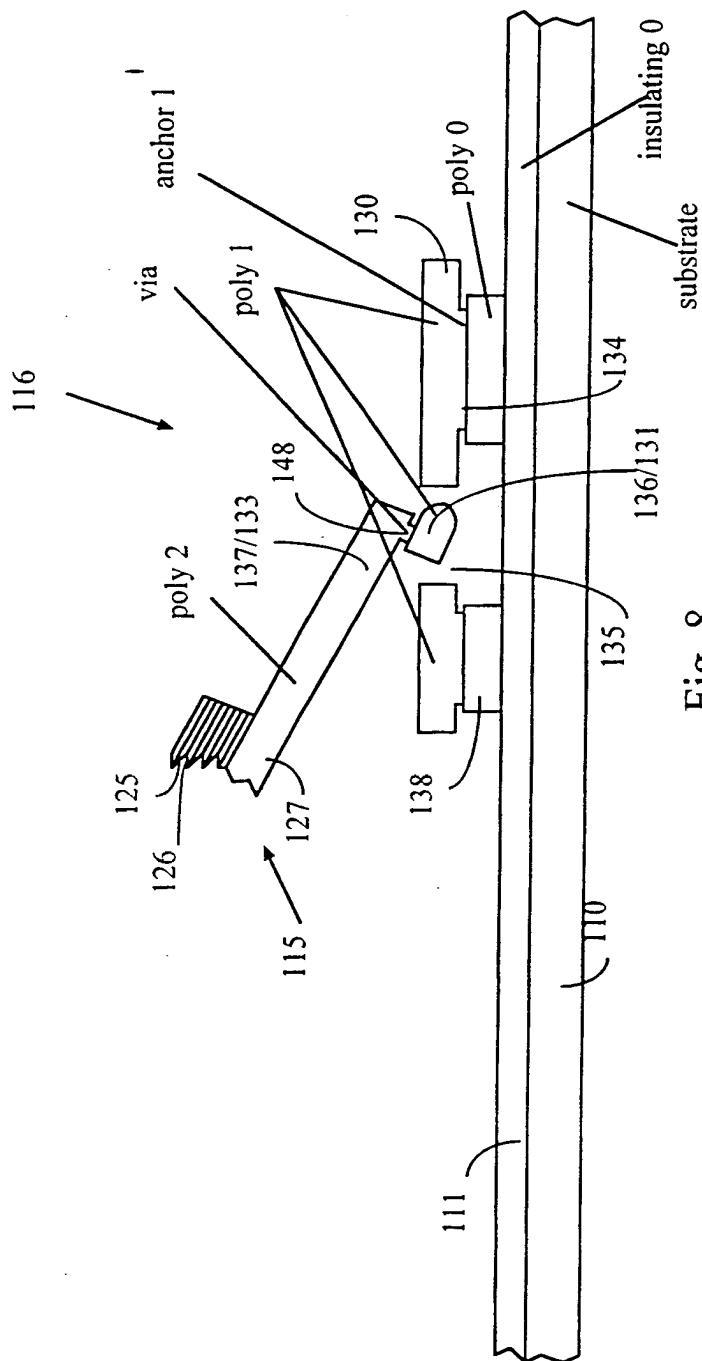


Fig. 8

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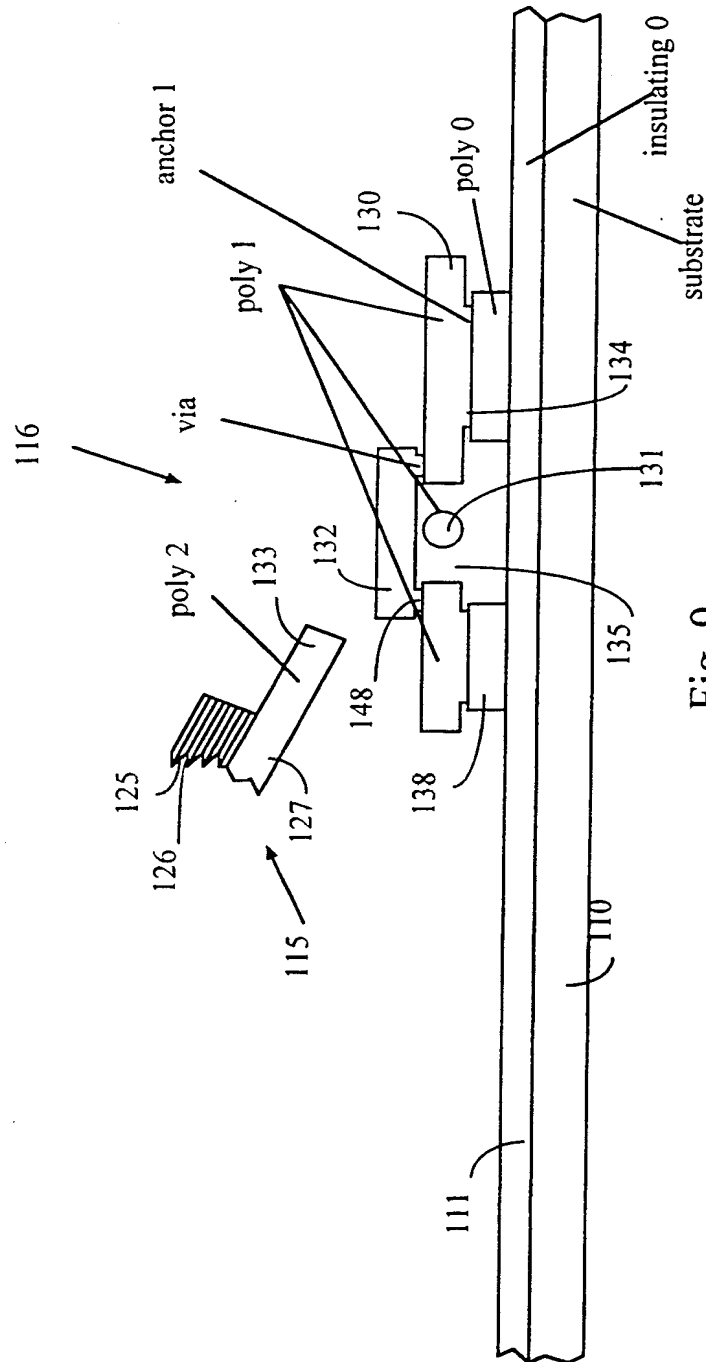


Fig. 9

FIG. 9



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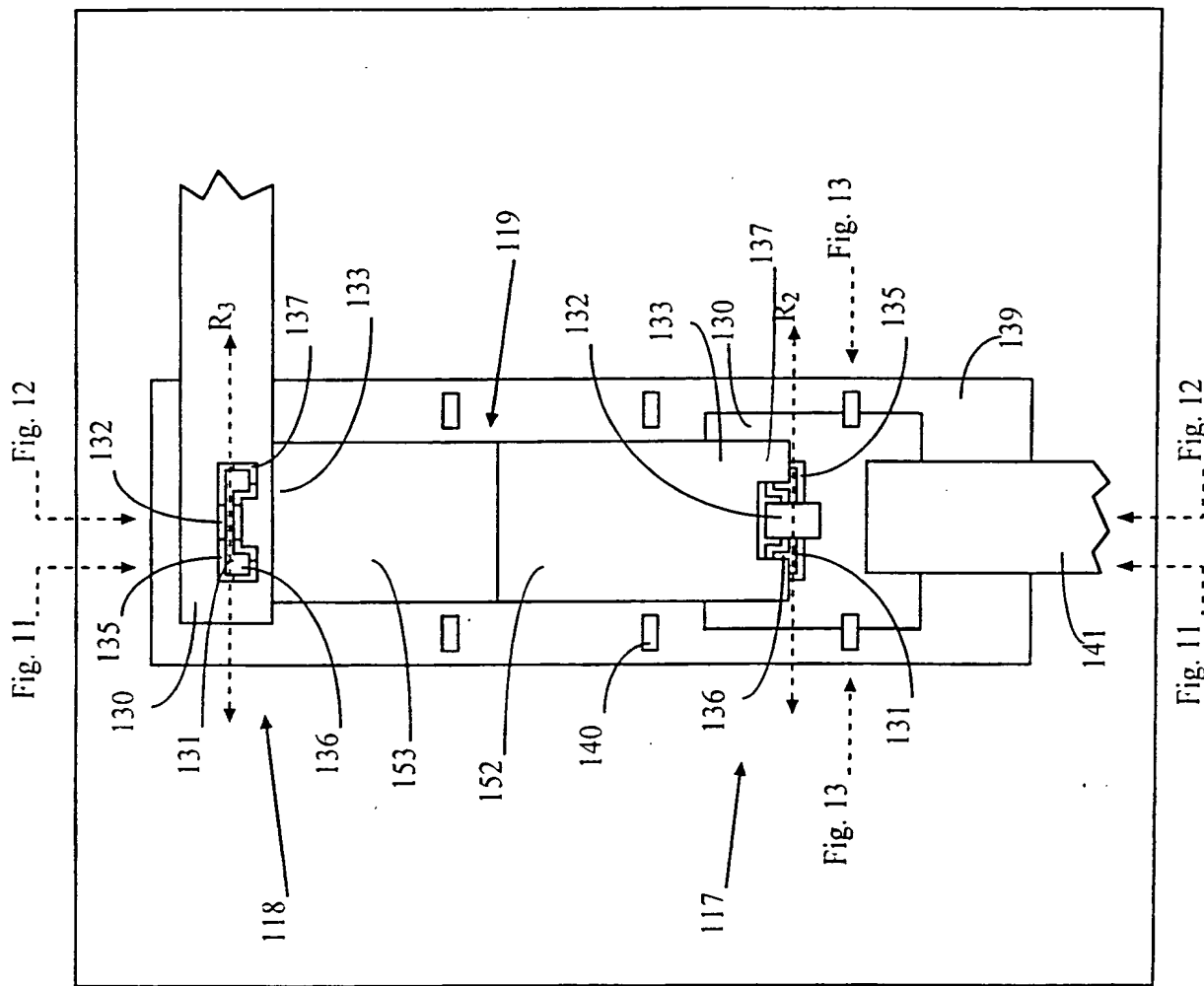


Fig. 10

FIG. 10



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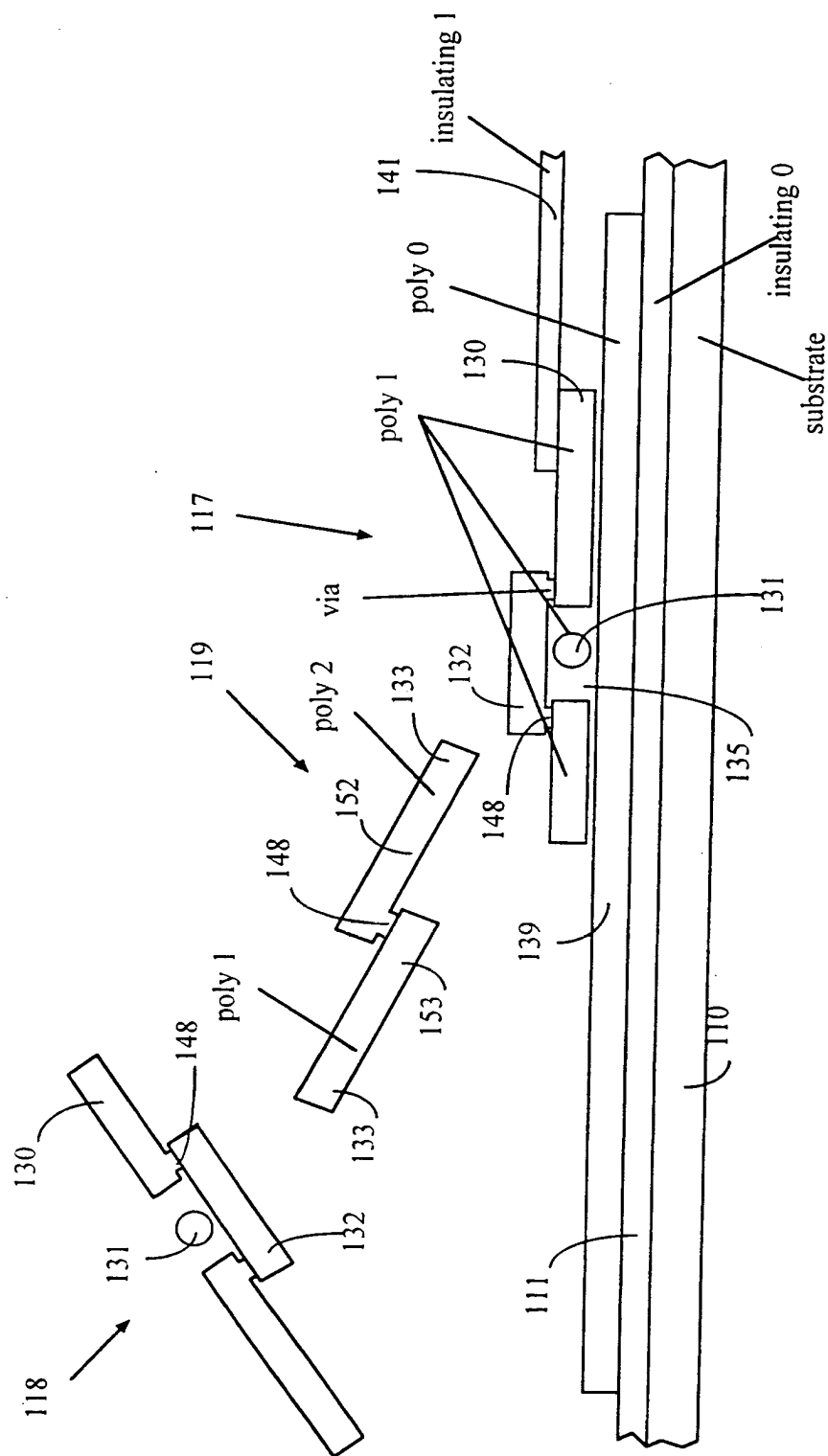


Fig. 12

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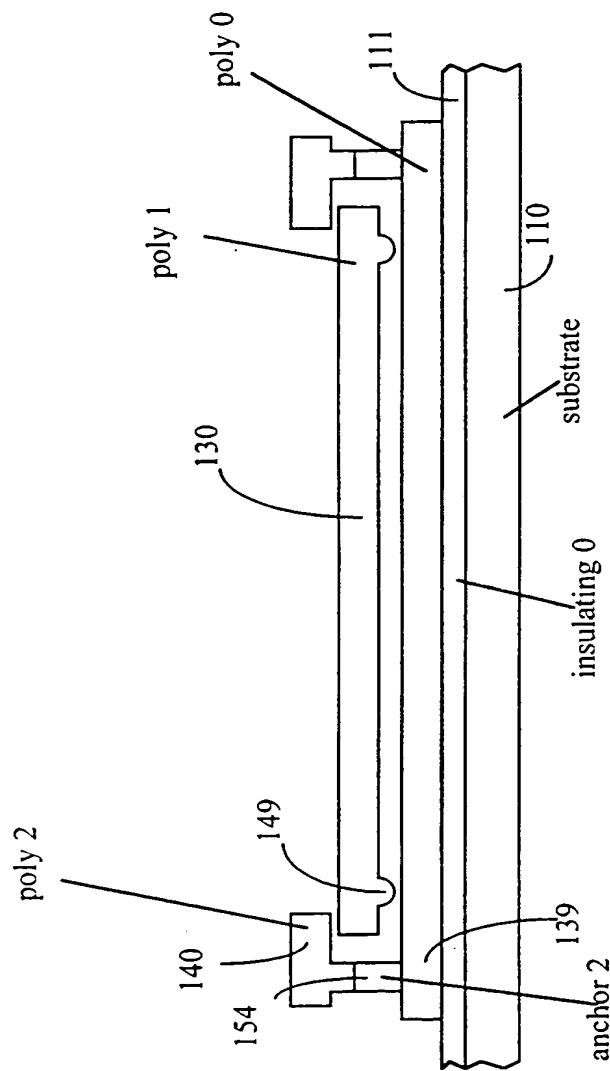


Fig. 13

FIG. 13

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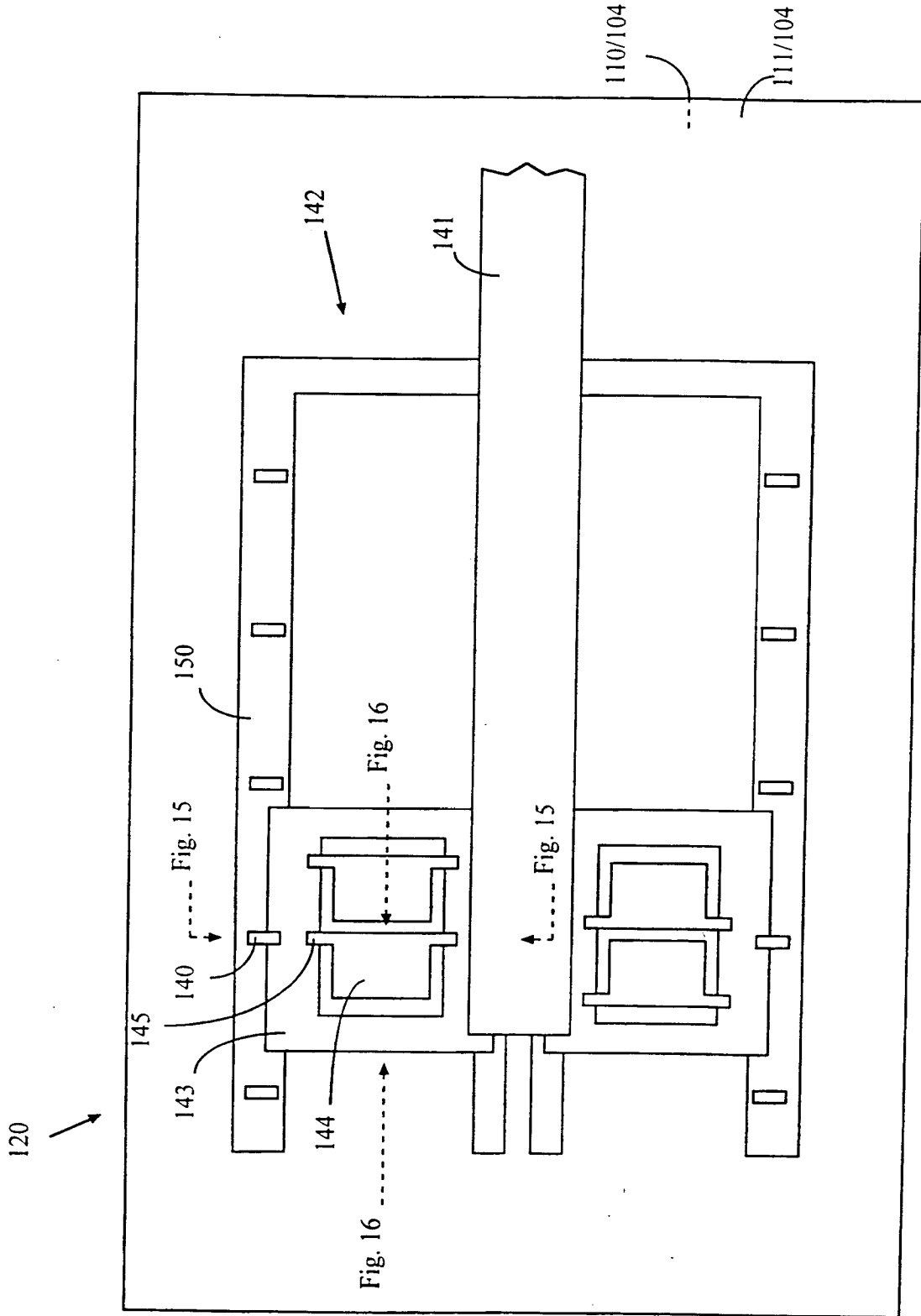


Fig. 14

insulating 1

poly 1

poly 0

141

145

via

146/144

147/144

139

148

insulating 0

substrate

110

111

143

140

154

From Control Circuit 109

150

anchor 2

Fig. 15

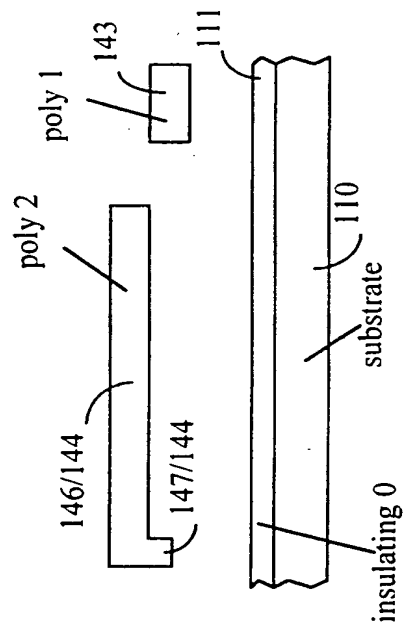
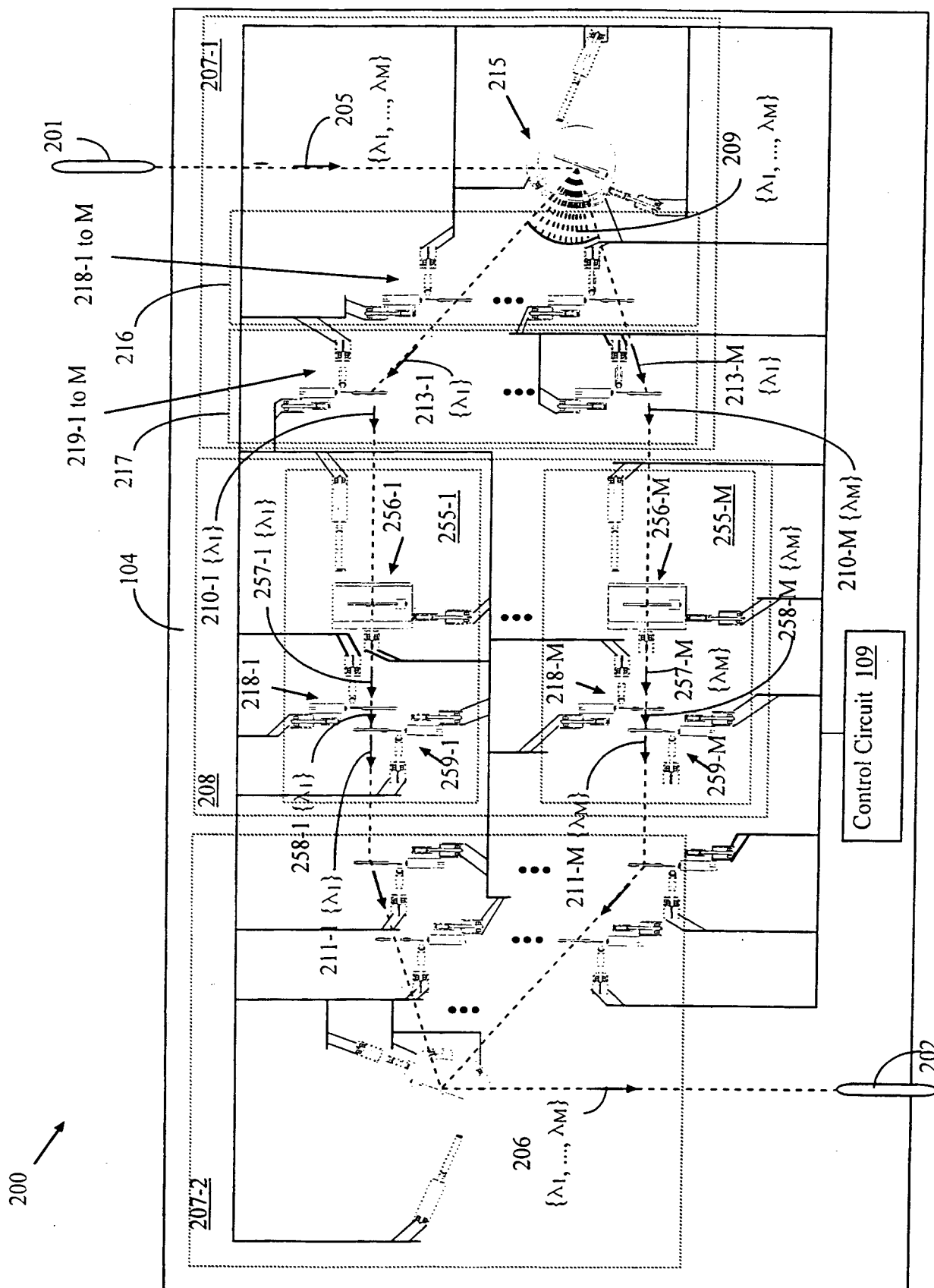


Fig. 16

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FIG. 18

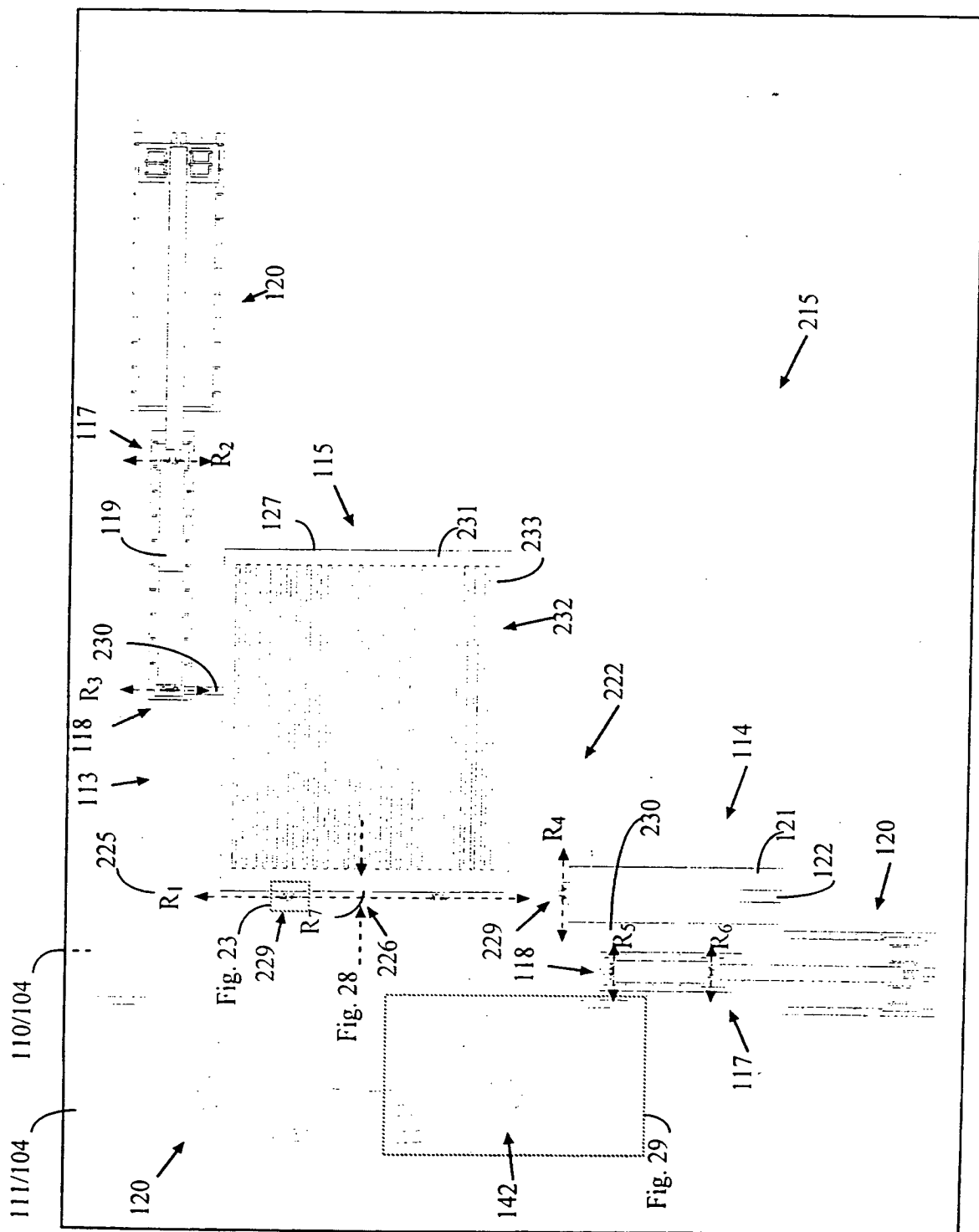


Fig. 18



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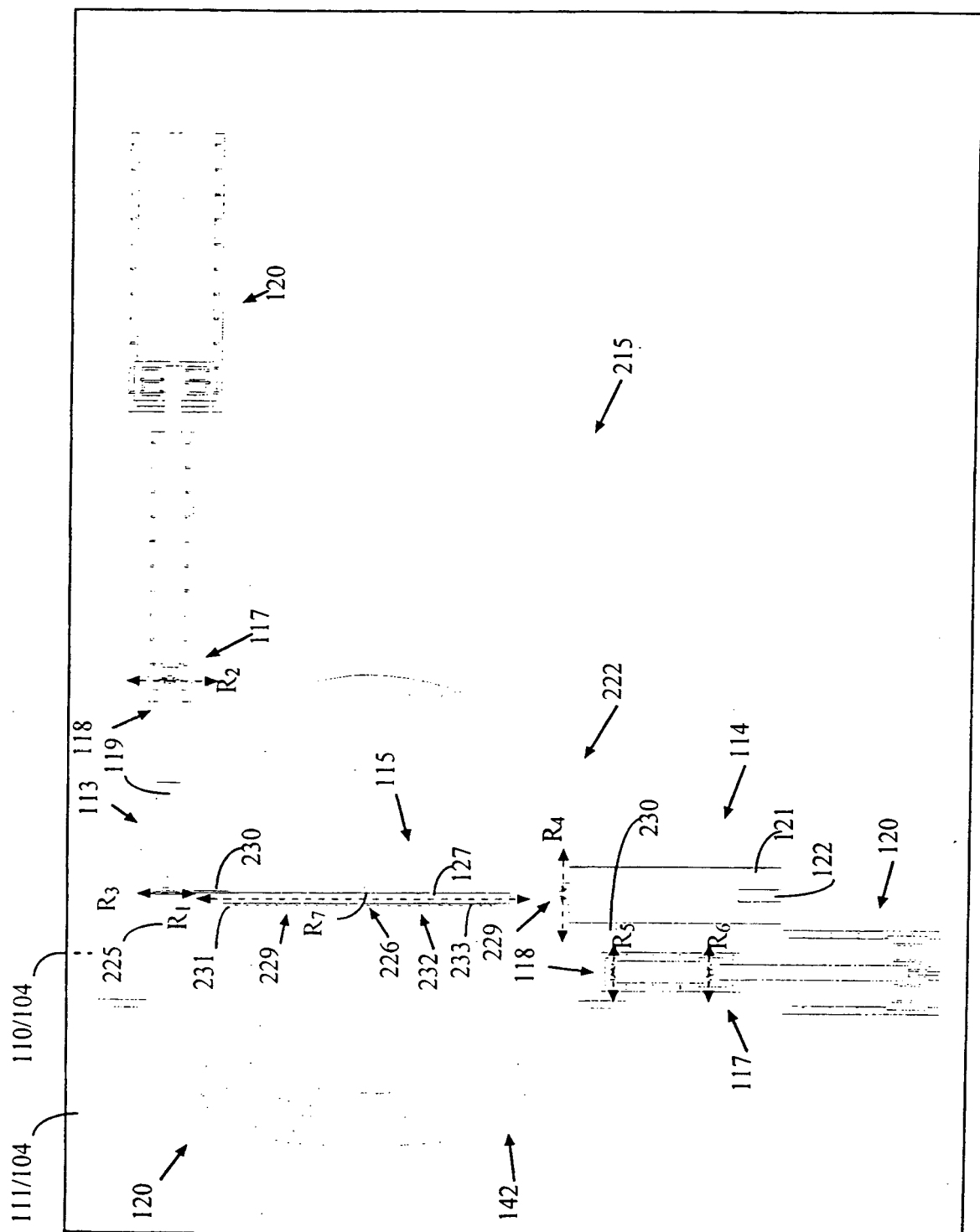


Fig. 19



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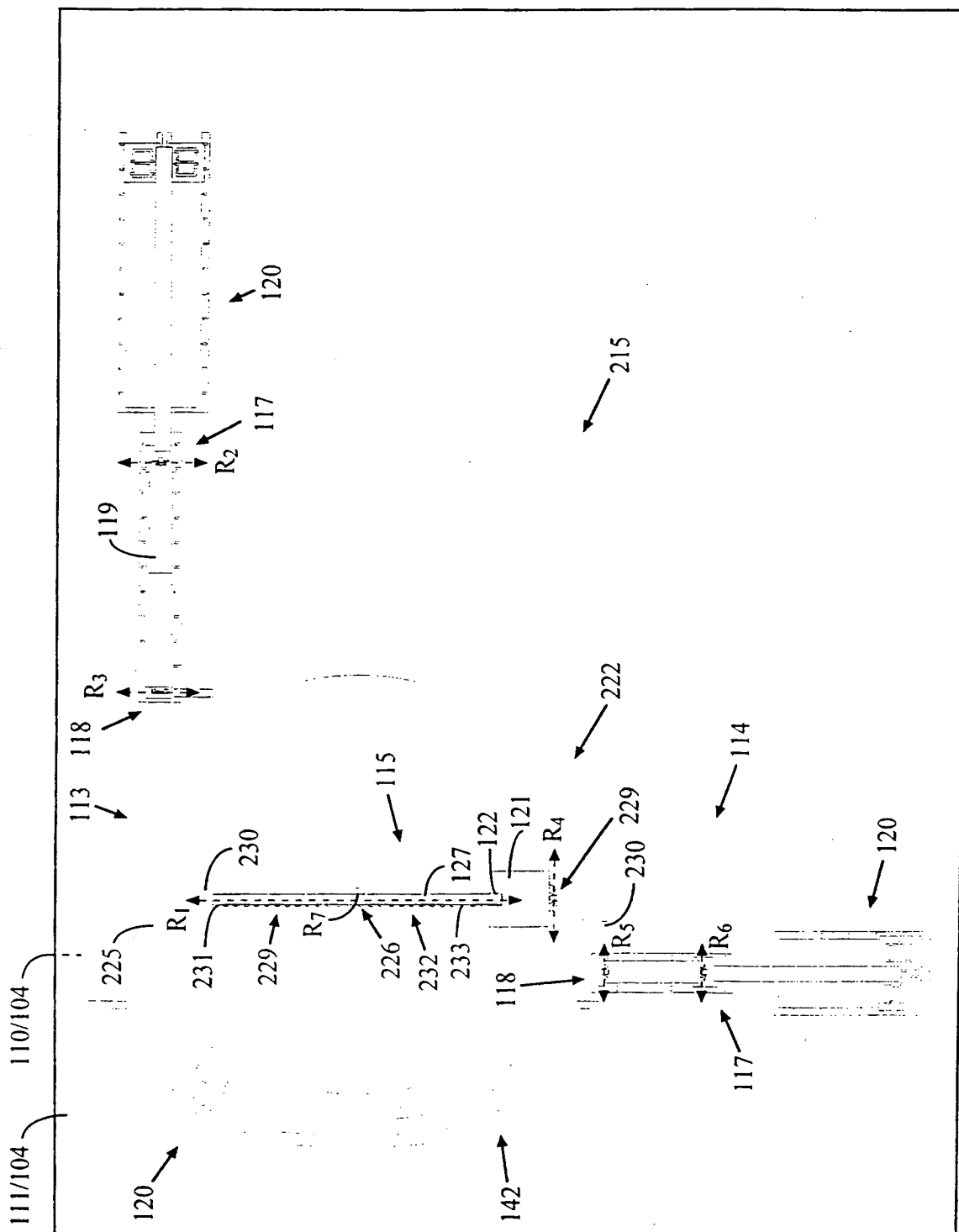


Fig. 21

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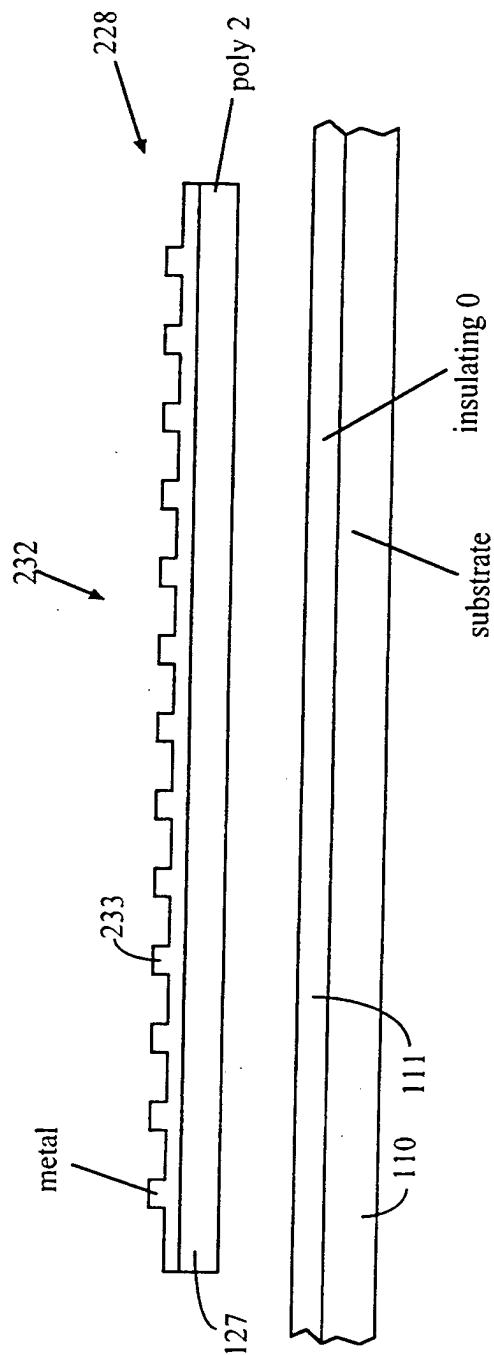


Fig. 22

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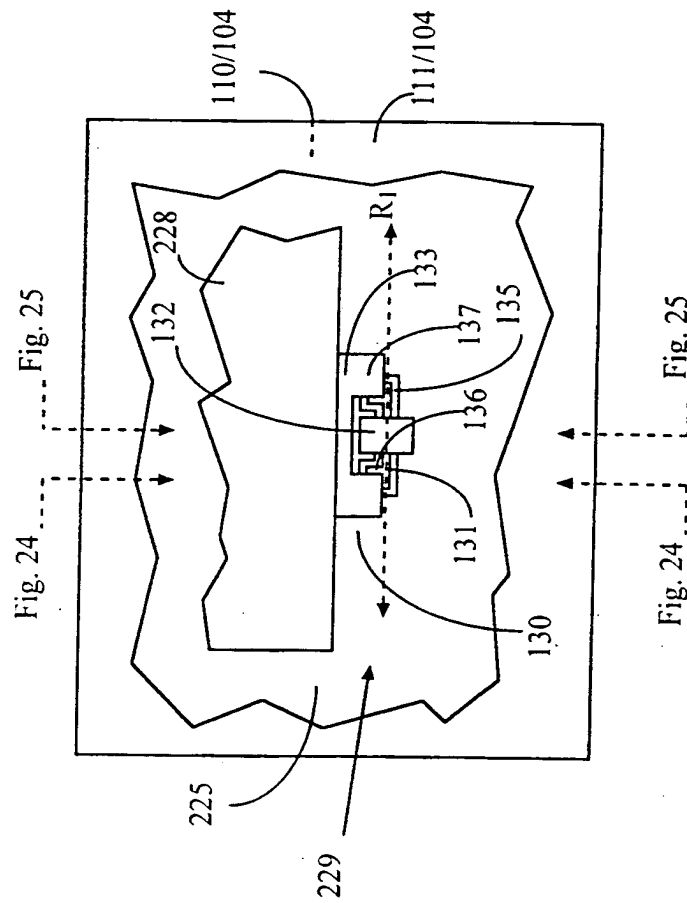


Fig. 23

FIG. 23

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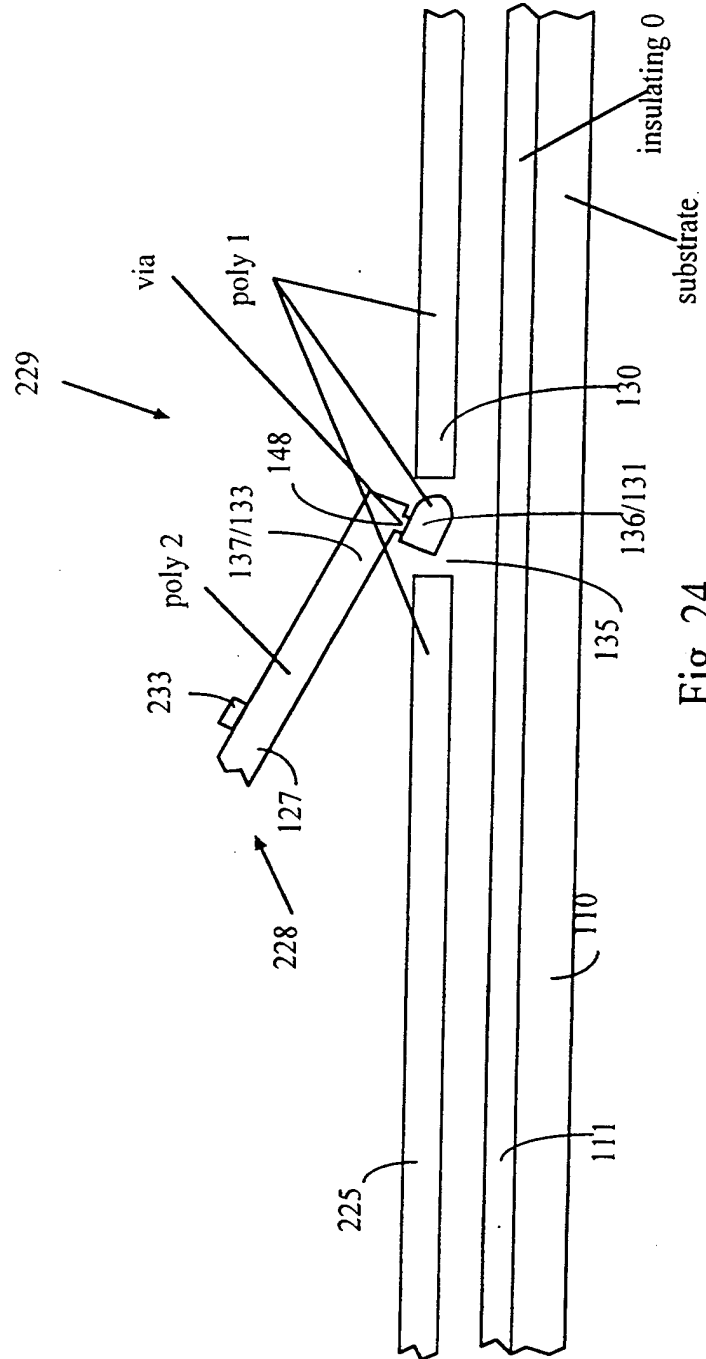


Fig. 24

FIG. 24

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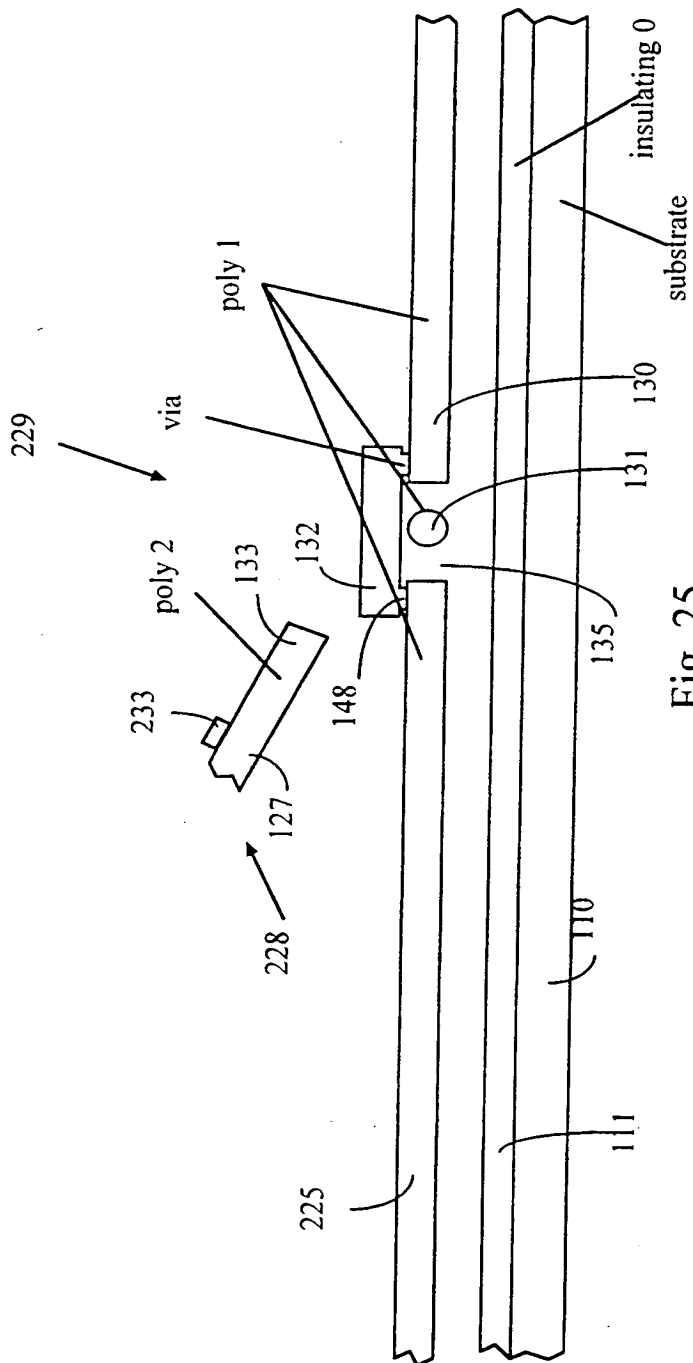


Fig. 25

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FIG. 27

Fig. 27

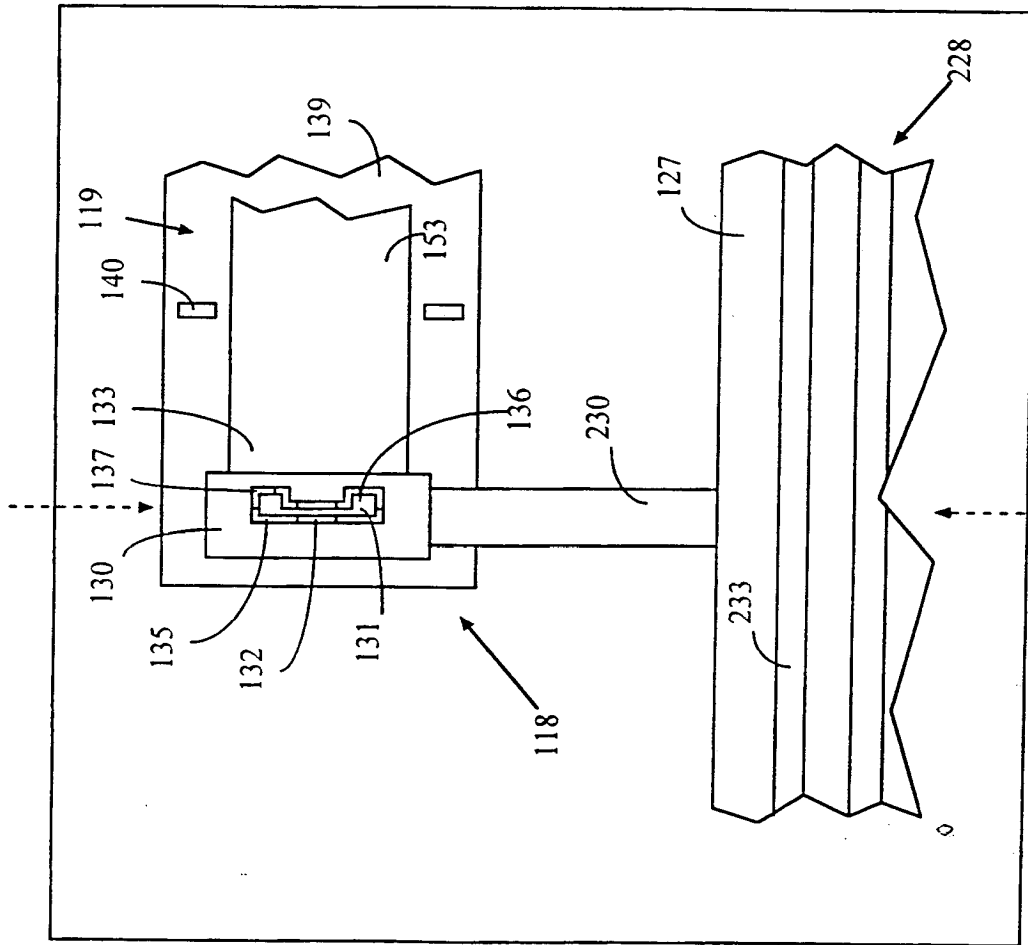
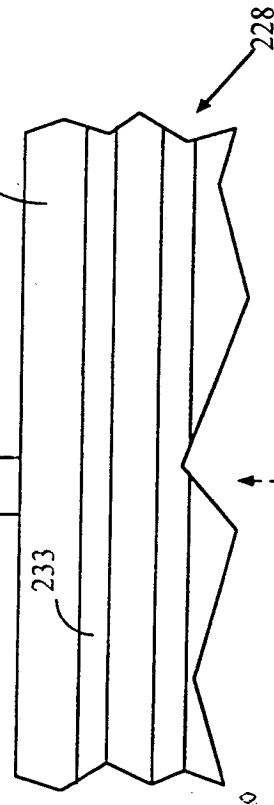


Fig. 27

Fig. 26







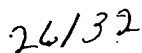


Fig. 28

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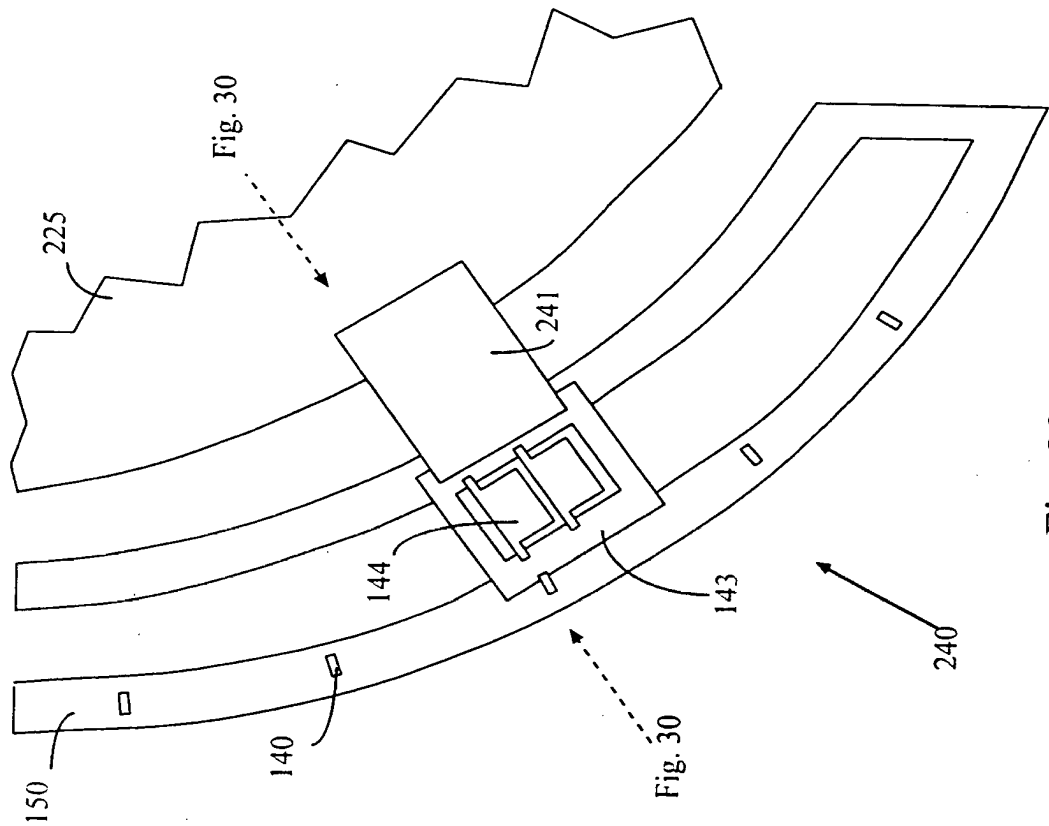


Fig. 29

FIG. 29

From Control Circuit 212

143

140

154

149

poly 2

via

146/144

147/144

139

148

insulating 0

substrate

110

poly 0

241

242

poly 1

225

111

anchor 2

150

Fig. 30

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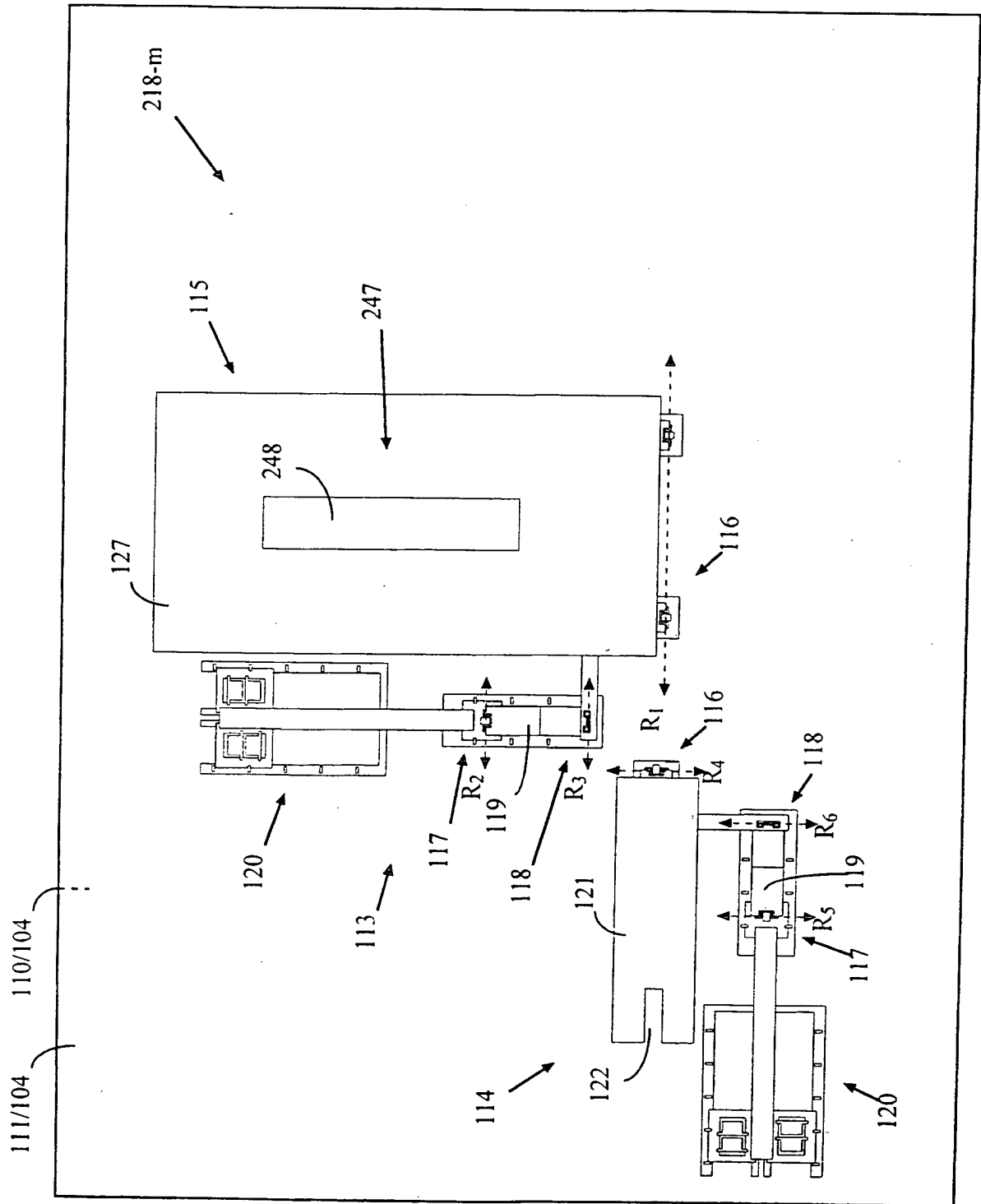


Fig. 31

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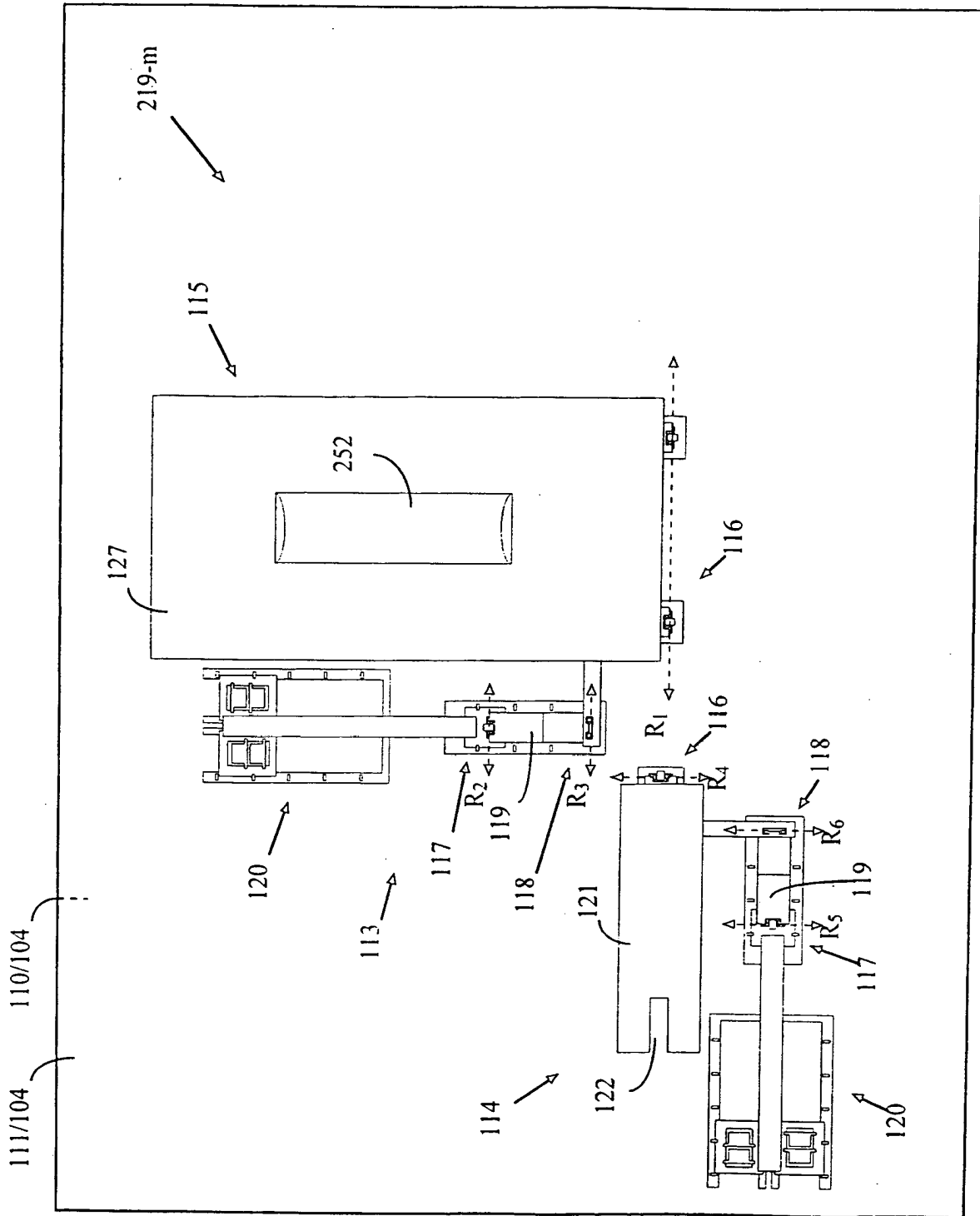


Fig. 32

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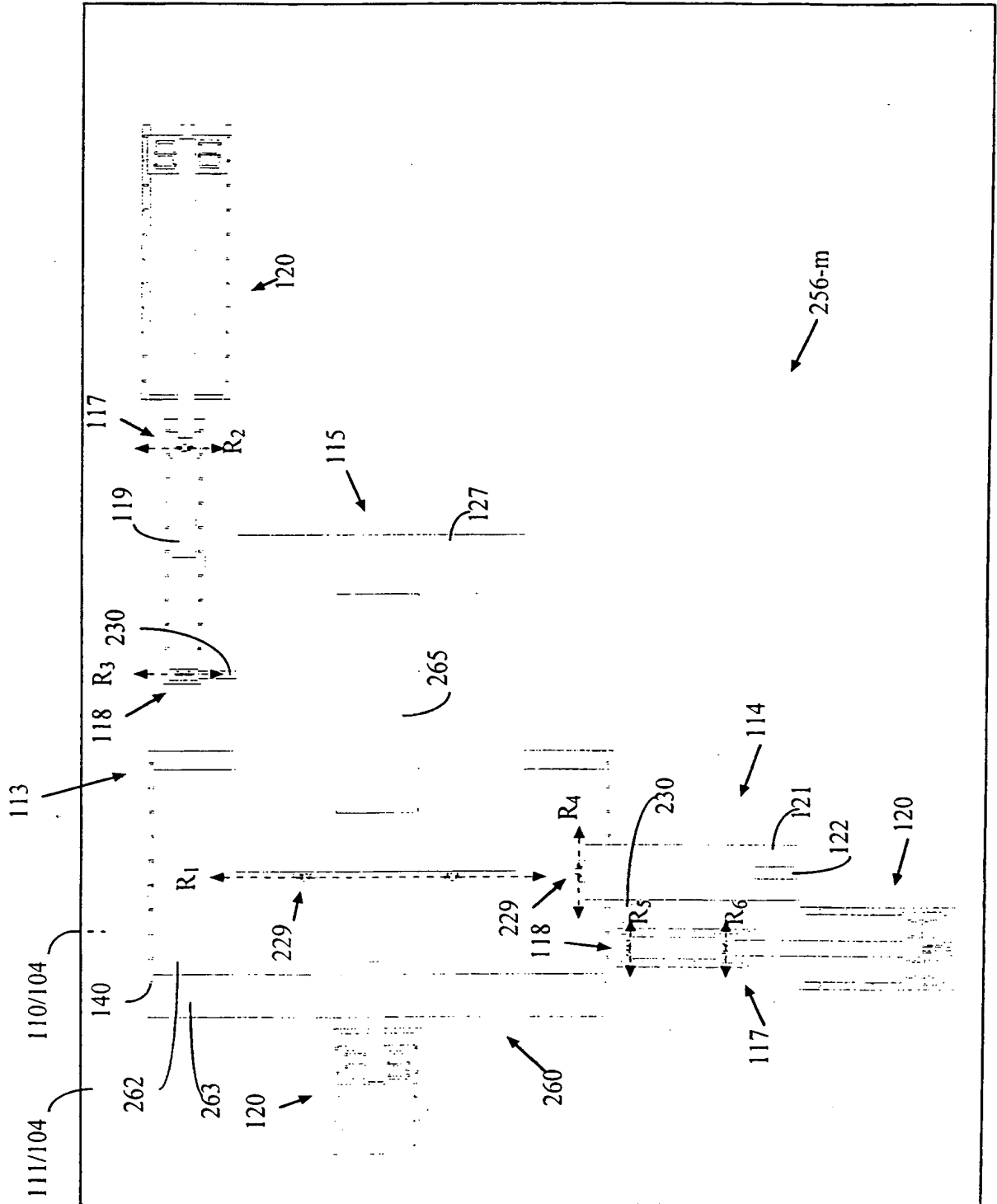


Fig. 33

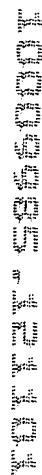


Fig. 34